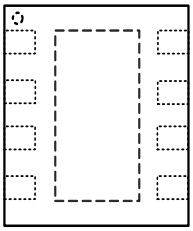
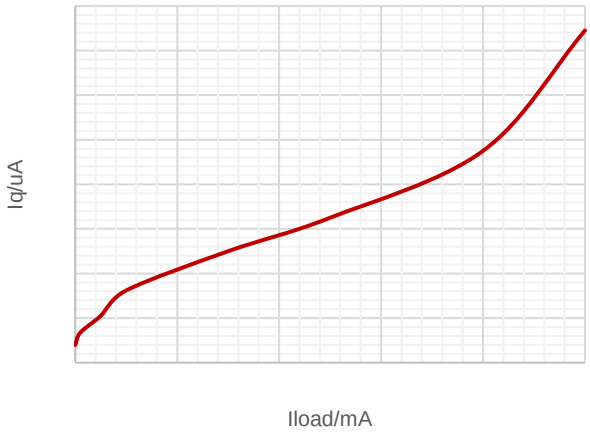
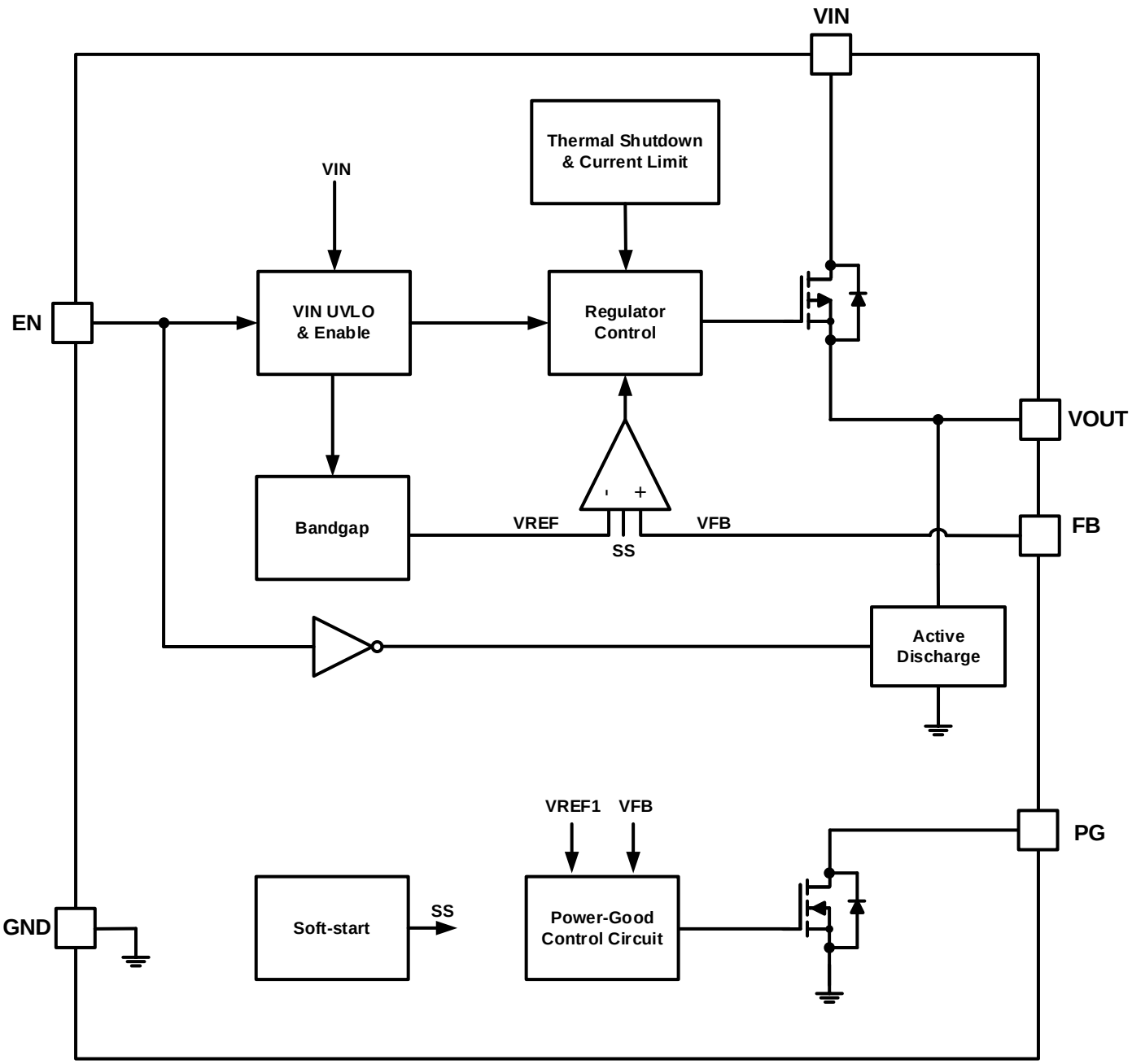
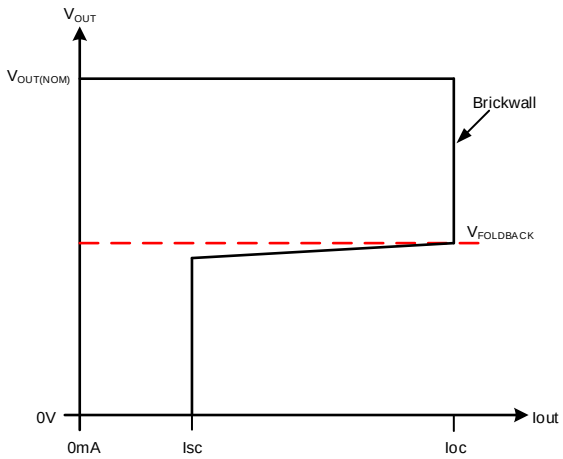



Iq/uA
Iload/mA


VIN
Thermal Shutdown
& Current Limit
VIN
EN
VIN UVLO
& Enable
Regulator
Control
VOUT
Bandgap
VREF
VFB
FB
SS
Active
Discharge
VREF1
VFB
PG
GND
Soft-start
SS
Power-Good
Control Circuit



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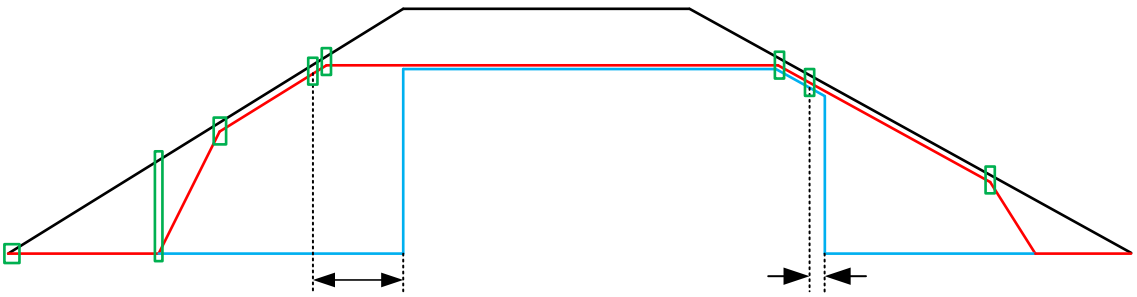
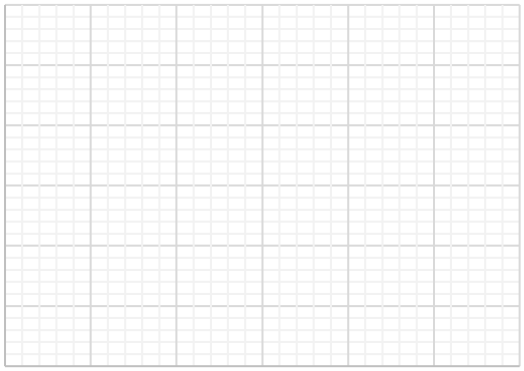
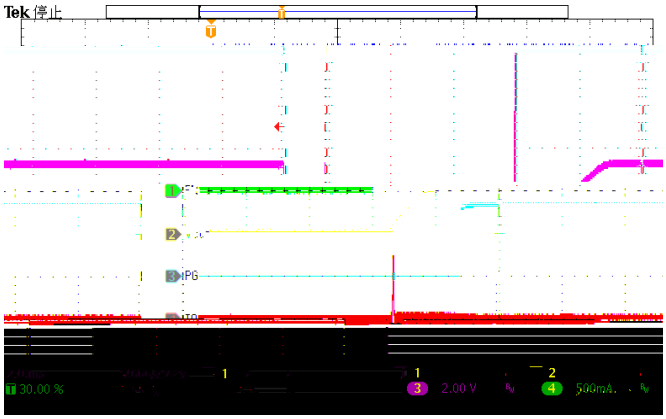
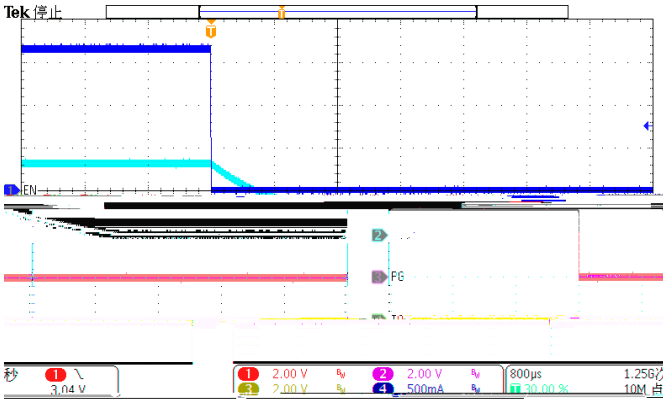
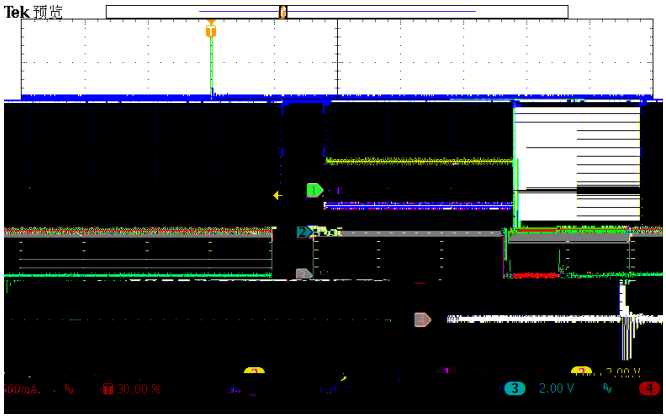
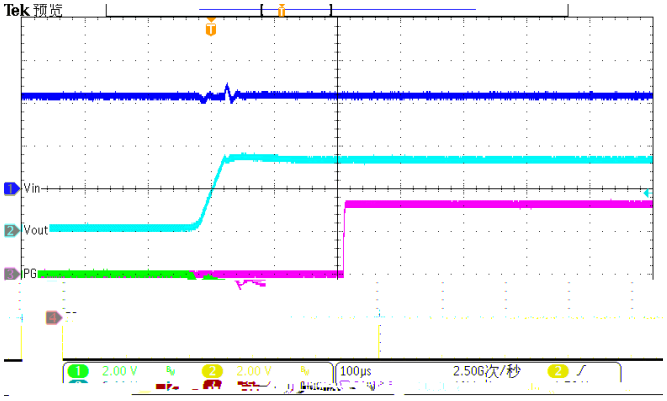


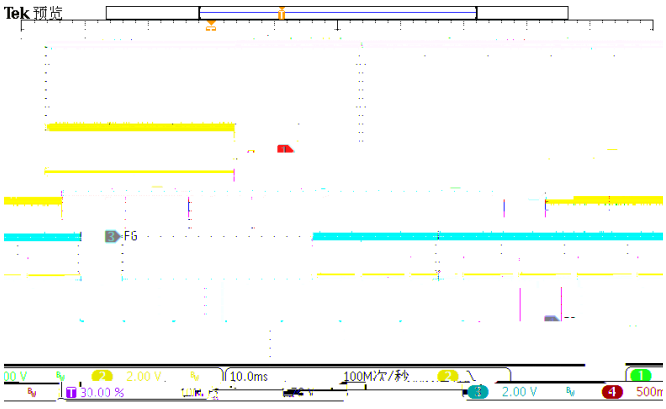


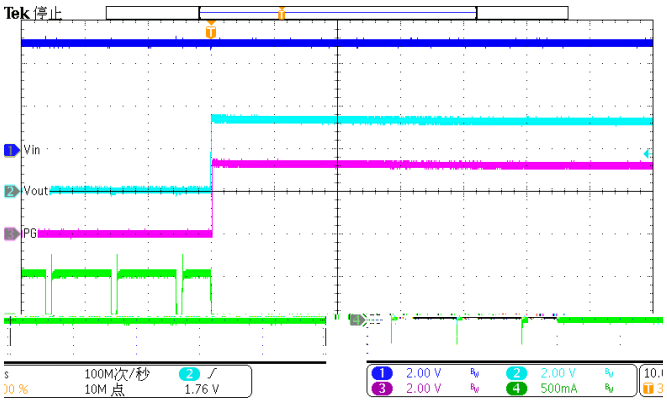

Tek 停止
PG
30.00 %
2.00 V
500mA


Tek 停止
EN
PG
2.00 V
2.00 V
500mA
800µs
30.00 %


Tek 预览
30.00 %
2.00 V


Tek 预览
Vin
Vout
PG
2.00 V
2.00 V
100µs
2.50G次/秒

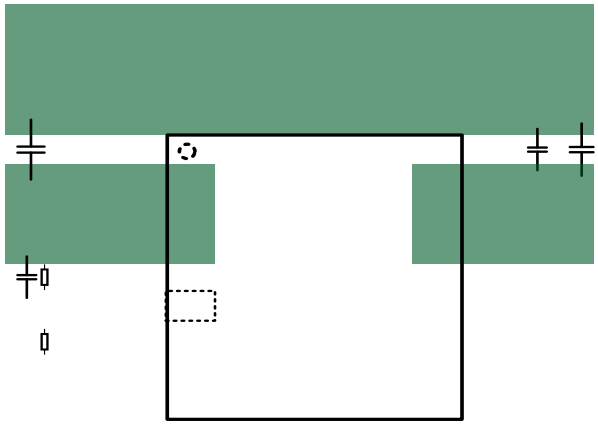

Tek 预览
FG
10.0ms
100M次/秒
2.00 V
500m

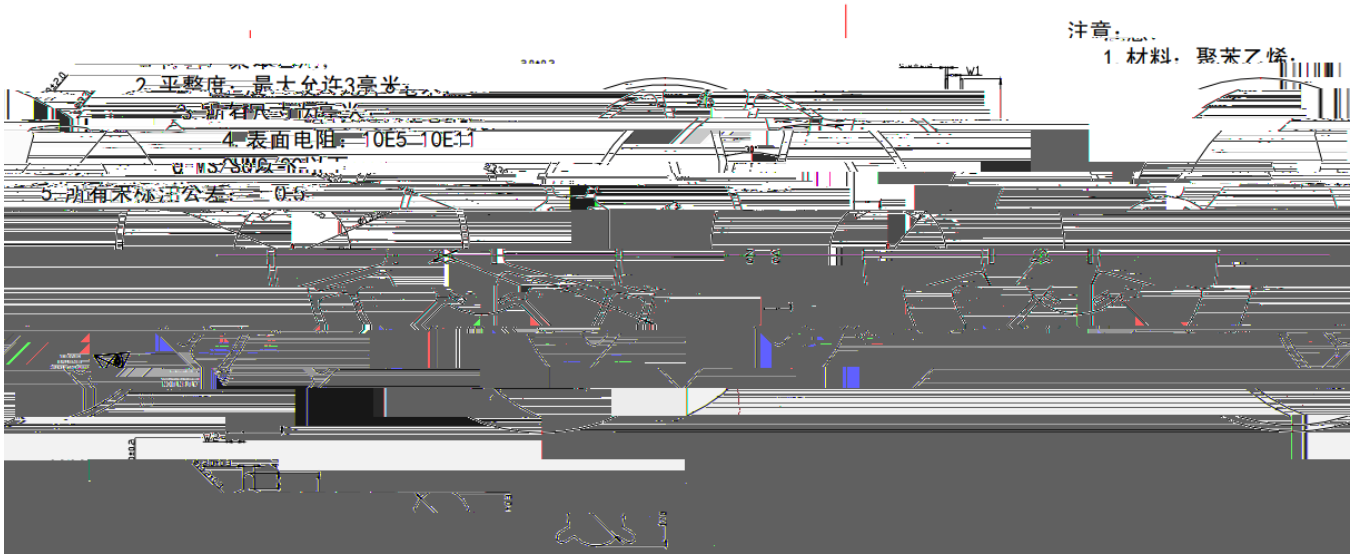

Tek 停止
Vin
Vout
PG
100M次/秒
10M 点
1.76 V
2.00 V
2.00 V
2.00 V
500mA

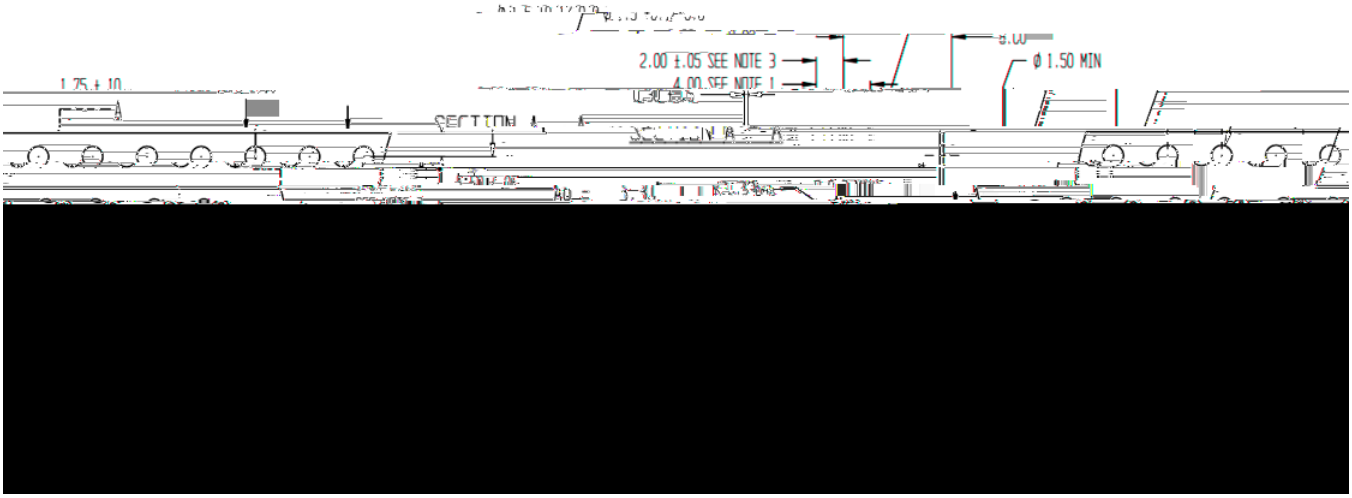
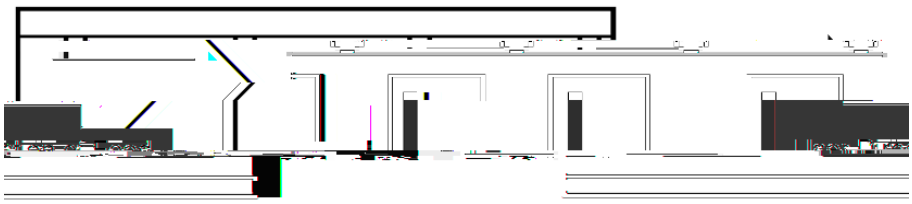
1.
2.

3.
4.

5.




注意：
1. 材料：聚苯乙烯。
2. 平整度：最大允许3毫米。
4. 表面电阻：10E5—10E11
5. 所有未标注公差：±0.5


2.00 ±.05 SEE NOTE 3
Ø 1.50 MIN
SECTION