REVISION HISTORY

DEVICE ORDER INFORMATION

DEVICE	TYPE	PACK QTY	MARKING		DESCRIPTION

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	MIN	MAX	UNIT
		6	

PIN CONFIGURATION

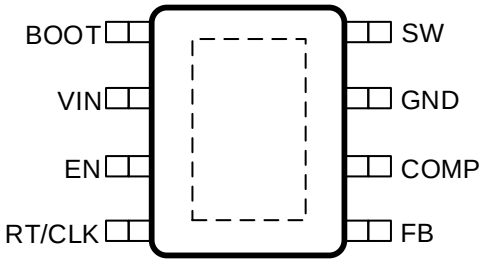


Figure 1. 8-Lead Plastic ESOP

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Current Limit and Over Current Protection						
Enable and Soft Startup						

Bootstrap Voltage Regulator and Low Drop-out Operation

APPLICATION INFORMATION

Typical Application

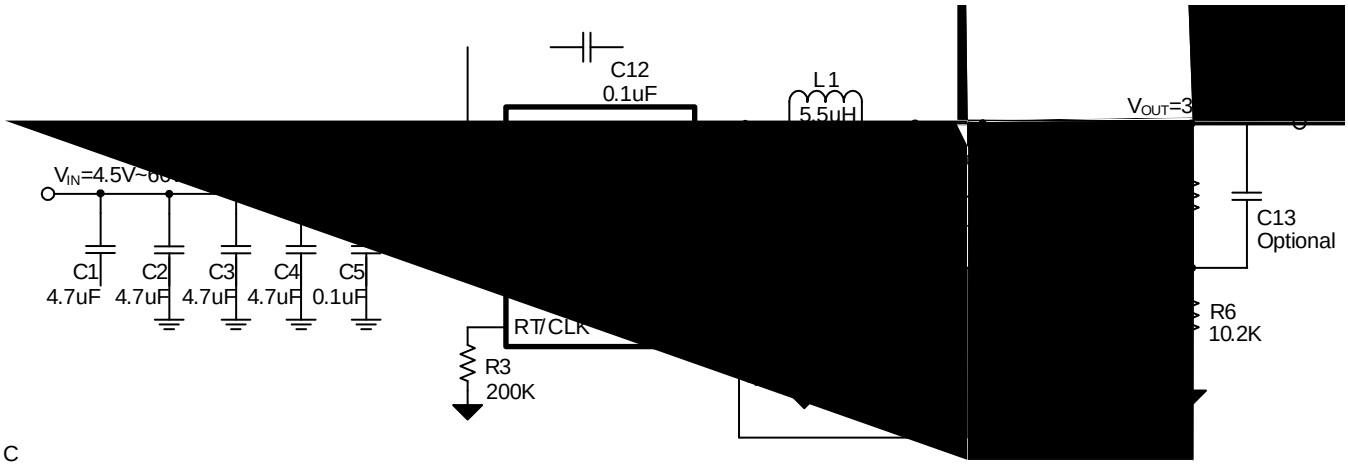


Figure 11. SCT2650Q Design Example, 3.3V Output with Programmable UVLO

Design Parameters

Design Parameters	Example Value

Inductor Selection

Diode Selection W R U

$$= \qquad \frac{\quad}{\quad}$$

$$_1 = \frac{1}{2 \qquad\qquad _7} = \frac{\qquad\qquad}{2 \qquad\qquad _7}$$

$$_2 = \frac{1}{2 \qquad\qquad\qquad} = \frac{\qquad\qquad\qquad}{2 \qquad\qquad\qquad}$$

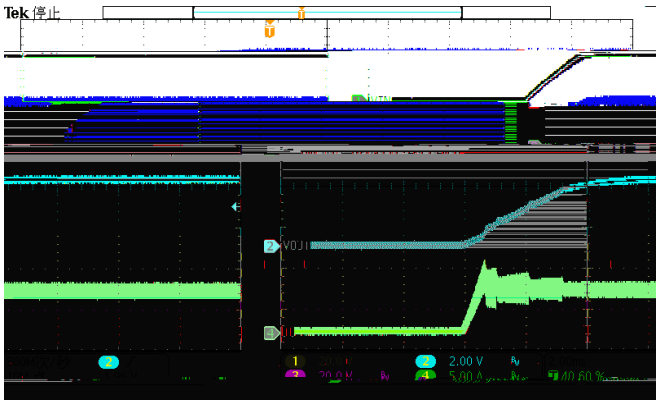
-
-
-

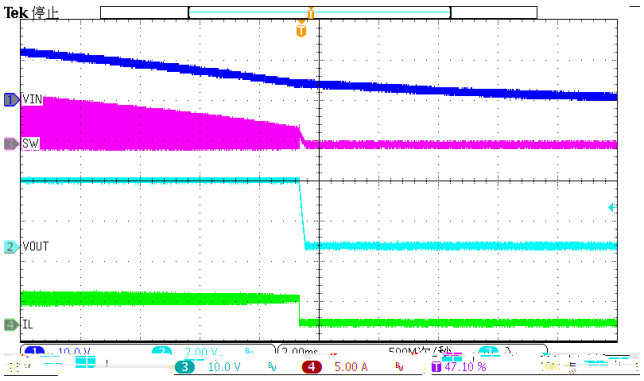
Table 3: Compensation Values for Typical Output Voltage/Capacitor Combinations at fsw=500KHz

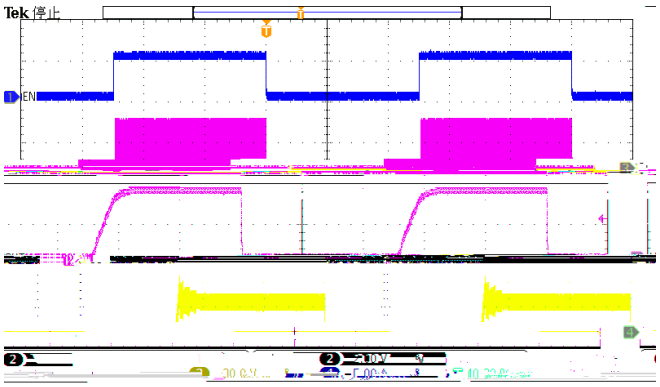
Vout	L1	COUT	R4	C7	C6
2.5V	4.7uH	4*47uF	16.9K	4.7nF	68pF (optional)
3.3V	5.5uH	4*47uF	20K	4.7 nF	47pF (optional)
5V	7.8uH	4*47uF	33.2K	3.3nF	22pF (optional)
12V	10uH	4*47uF	53.6K	1nF	220pF
24V	15uH	4*47uF	105k	1nF	220pF

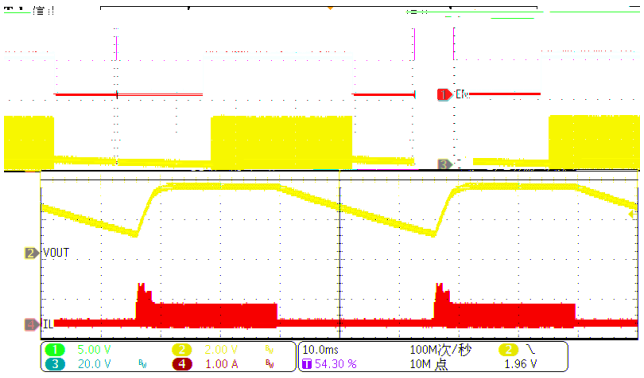
Inverting Power application

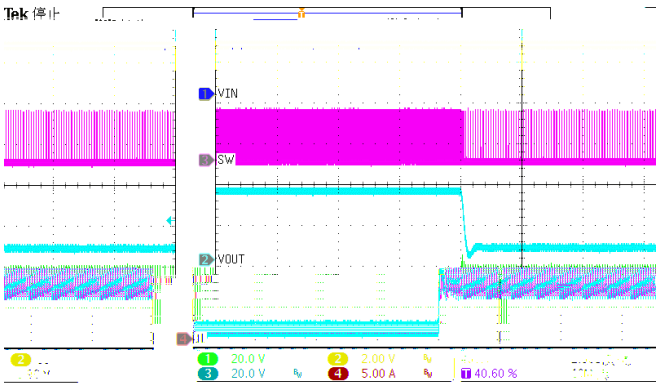
Application Waveforms

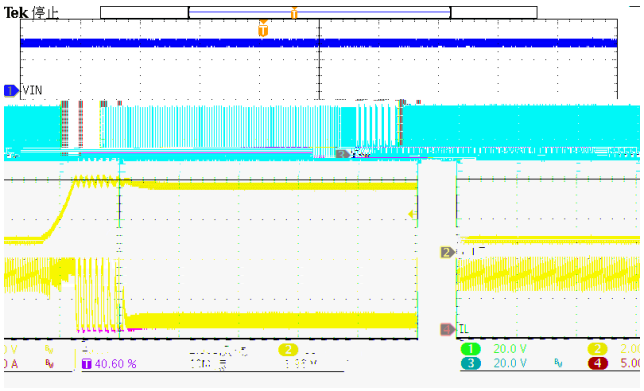




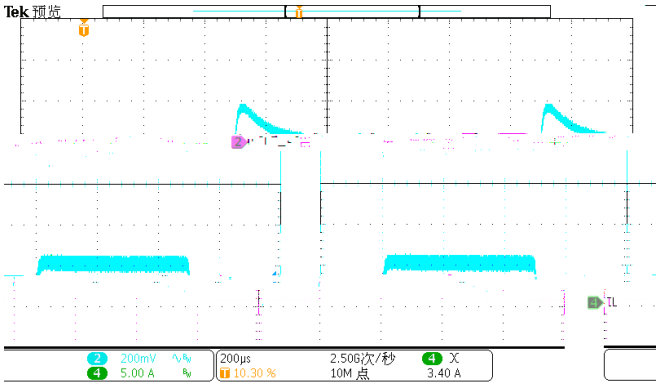


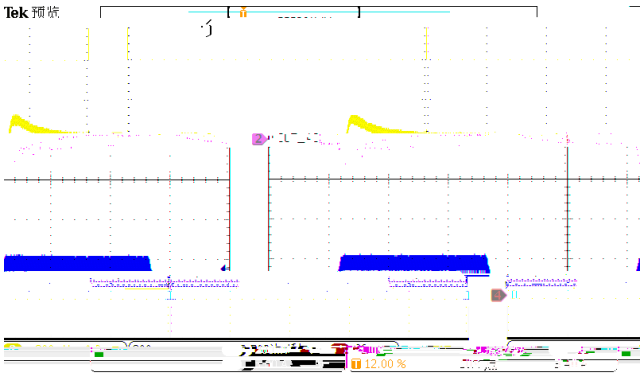


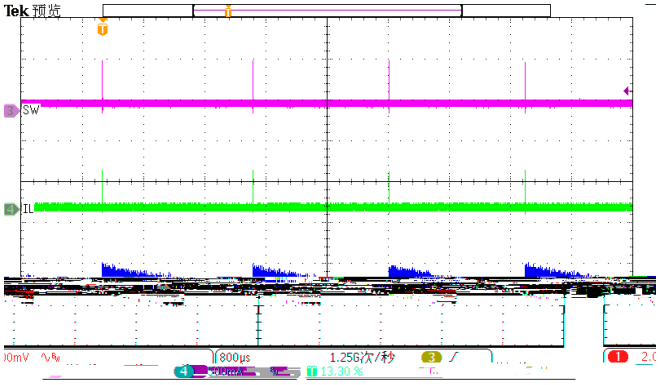


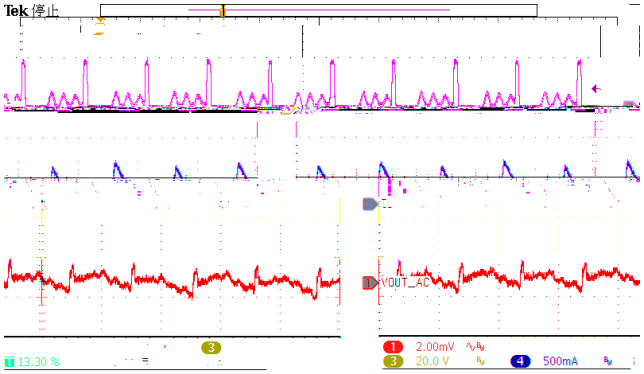


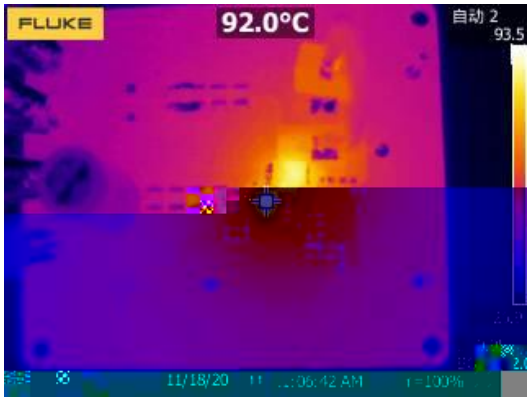
Application Waveforms(continued)

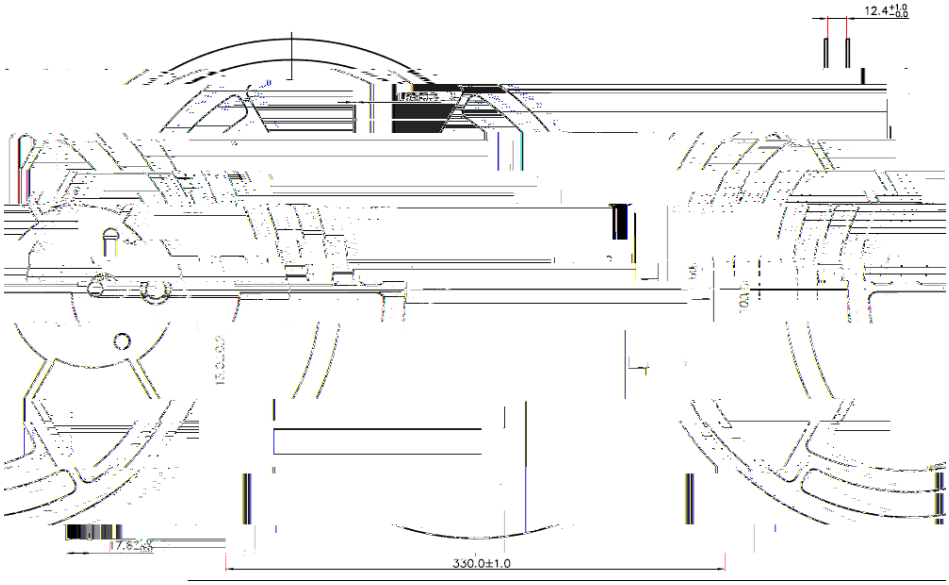
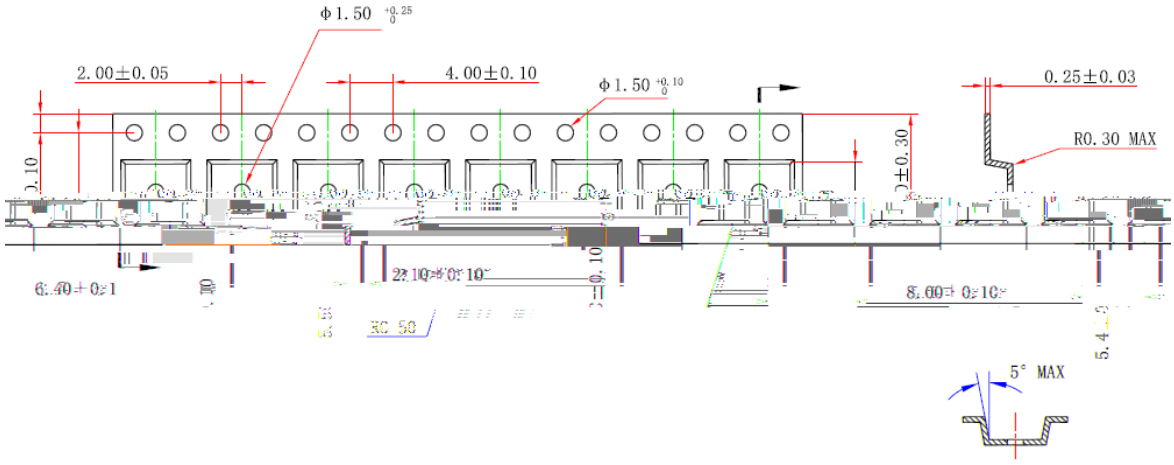










Layout Guideline

TAPE AND REEL INFORMATION